



PATENT APPLICATION

Applicant: Seok-Jun WON Serial No. 10/676,865
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Title: FLAT-TYPE CAPACITOR FOR INTEGRATED CIRCUIT AND METHOD OF
MANUFACTURING THE SAME

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class
<u>Loke</u>	_____	6,271,596	Aug. 7, 01	Alers		

FOREIGN PATENT DOCUMENTS

Exam Init	Ref	Document Number	Publication Date	Country	Name
<u>Loke</u>	_____	2000-228497	Aug. 15, 00	Japan	Lee Hyae-Ryoung

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
<u>Loke</u>	_____	English Language of Abstract for Japanese Patent Publication No. 2000-228497

Examiner: Loke

Date Considered: 6/2/05